

**Exam : HP HP0-085**

**Title : Planning and Designing HP  
Superdome Server  
Solutions**

**Version : Demo**

1. Which high availability cluster solution (HA Cluster) is available for both the HP-UX 11i and Linux operating systems for HP Integrity Superdome?

- A. TruCluster
- B. Lifekeeper cluster
- C. DoubleTake Failover
- D. Serviceguard Cluster

**Answer: D**

2. What is the maximum number of hard partition (nPar) servers keyboard, video and mouse connections that can be connected directly to the HP KVM IP console?

- A. 8
- B. 16
- C. 32
- D. 64

**Answer: B**

3. What are three components of an HP Superdome High Level Design (HLD)? (Select three.)

- A. Visio drawings
- B. budgetary quote
- C. Solution Implementation Plan (SIP)
- D. Solution Integration Service Plan (SISP)
- E. Site Environment Readiness Review (SERR)

**Answer: ABE**

4. Which component coordinates memory access between sx1000 cell boards within the same hard partition (nPAR)?

- A. XPC chip
- B. mx2 module
- C. SerDes Chip
- D. cell controller chip

**Answer: D**

5. What can be used to set the memory allocation for cell local memory? (Select two.)

- A. parmgr utility
- B. partition magic utility
- C. nPartition commands
- D. process resources manager

**Answer: AC**

6. What is the role of the processor-dependent hardware (PDH) module in an sx1000 or sx2000 Superdome server?

- A. connects the cell to the I/O subsystem
- B. coordinates the boot process using firmware
- C. activates spare processors when additional processing capacity is needed
- D. multiplexes and de-multiplexes data between the controller chip and the SDRAM in the memory subsystem

**Answer: B**

7. How does Global Shared Memory affect sx2000 Superdome server performance? (Select three.)

- A. requires fewer Network Interface Cards
- B. provides console functionality for each cell in a Superdome server
- C. provides direct data transfer across partitions using cross-partition interrupts
- D. provides direct remote access to the cell through the Management Processor (MP)
- E. shifts networking between partitions to the backplane instead of through I/O subsystems

**Answer: ACE**

8. Which feature of the sx1000 chipset was preserved on the sx2000 chipset to maintain high-availability?

- A. realtime cache correction
- B. quad crossbar switching fabric
- C. self-healing crossbar and I/O pathways

D. cell and PCI online addition, replacement and deletion

**Answer: D**

9. What is the maximum number of processor modules and DIMMs that can be installed in a single cabinet sx2000 based Integrity Superdome hardware partition?

A. 16 single-core or dual-core processor modules and up to 128 DIMMs

B. 32 single-core or dual-core processor modules and up to 128 DIMMs

C. 32 single-core or dual-core processor modules and up to 256 DIMMs

D. 64 single-core or dual-core processor modules and up to 256 DIMMs

**Answer: C**

10. What is the maximum number of sx2000-based cell boards that can be installed in a single HP Integrity Superdome cabinet that supports up to 64 processor cores?

A. 2

B. 4

C. 8

D. 16

**Answer: C**